

ABSTRACT OF THE DISCLOSURE

1 A flat panel display capable of preventing inline short between adjacent wirings and voltage
2 drop through power supply line by using pixel electrode layer as a power supply layer, and a
3 fabrication method thereof. A flat panel display of the present invention is made up of a thin film
4 transistor including source/drain electrodes, formed on an insulation substrate, an insulation film
5 formed on the insulation substrate including the thin film transistor and including first and second
6 contact holes for exposing the source/drain electrodes respectively, a pixel electrode formed on the
7 insulation film and connected to one of the source/drain electrodes through one of the first and
8 second contact holes, and a power supply layer formed on the insulation film and connected to the
9 other one of the source/drain electrodes through the other one of the first and second contact holes.
10 Losses in the power supply line is reduced by forming the power supply line of a low resistivity
11 material and by providing the power supply line in a grid structure.
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